

Roll No. ....

**23257**

**M. Tech. 1st Sem. (Electrical Engg.)  
Specialization : Electrical Power Systems  
Examination – December, 2014**

**MICRO PROCESSOR & MICRO CONTROLLER**

Paper : MTEPS-101

*Time : Three Hours ]*

*[ Maximum Marks : 100*

*Before answering the questions, candidates should ensure that they have been supplied the correct and complete question paper. No complaint in this regard, will be entertained after examination.*

*Note : Students have a attempts any **five** questions out of **eight** questions. All questions carry equal marks.*

1. Explain the physical memory organisation and addressing modes of 8086. 20
2. Discuss the minimum mode 8086 system and its timings. 20
3. Discuss the instruction set and machine language instruction format of 8086 in detail. 20

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4. Write short notes on : 20
- (i) Interrupts I/D.
  - (ii) Block Transfer and DMA.
5. Discuss the slack structure of 8086/8088 and also discuss interrupts cycle of 8086. 20
6. Explain the following : 20
- (i) Interfacing of D/A converter with control used.
  - (ii) Stepper motor Interfacing.
7. Discuss the display controller 8279 programmable communication Interface 8251 USART in detail. 20
8. Write short notes on : 20
- (a) Addressing mode and Instruction set of 8051.
  - (b) Programmable Interval timer 8254.